



Docket No.: R2180.0121/P121  
(PATENT)

#34  
D. M. M. M.  
P-1003  
P. M. M. M. B

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Masahiro Matsuo et al.

Application No.: 10/075,624

Group Art Unit: Not Yet Known

Filed: February 15, 2002

Examiner: Not Yet Known

For: METHOD AND APPARATUS FOR  
POWER SUPPLY CAPABLE OF  
EFFECTIVELY REDUCING POWER  
CONSUMPTION

SECOND PRELIMINARY AMENDMENT

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

Please amend the application as follows:

IN THE SPECIFICATION:

Please replace paragraph 0088 with the following:

During the sleep mode, the overshoot preventive circuit 63 raises the voltage  $V_o$  to the level of the power source voltage  $V_{DD}$  by making the gate voltage of the P-MOS transistor 41 low to turn on the P-MOS transistor 41 when the detected current is smaller than a predetermined value  $\alpha$  (e.g., 1A). When the detected current is greater than the predetermined value  $\alpha$  (e.g., 1A), the overshoot preventive circuit 63 continuously raises the gate voltage of the P-MOS transistor 41 in response to the detected current so that the

B1